

<b>PCN Number:</b>	20181119000.0	<b>PCN Date:</b>	November 27, 2018
<b>Title:</b>	Datasheet for TMS320F28050 - TMS320F28055		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

### Notification Details

#### Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



**TMS320F28055, TMS320F28054, TMS320F28053  
TMS320F28052, TMS320F28051, TMS320F28050**

SPRS797C – NOVEMBER 2012 – REVISED OCTOBER 2018

Changes from July 2, 2014 to October 26, 2018 (from B Revision (July 2014) to C Revision)	Page
• <b>Global:</b> Changed "CAN 2.0B" to "ISO 11898-1 (CAN 2.0B)".	<a href="#">1</a>
• <b>Global:</b> Changed SDAA from "I/OC" to "I/OD". Changed SCLA from "I/OC" to "I/OD".	<a href="#">1</a>
• <b>Section 1.1 (Features):</b> Added "Temperature Options" feature.	<a href="#">1</a>
• <b>Section 1.2 (Applications):</b> Updated section.	<a href="#">2</a>
• <b>Section 1.3 (Description):</b> Added "TMS320C2000™ 32-bit microcontrollers are optimized for processing, sensing, and actuation ..." paragraph.	<a href="#">2</a>
• <b>Table 3-1 (Device Comparison):</b> Changed "PGA (Gains = ~3, ~6, ~11)" to "PGA (Gain ≈ 3, 6, or 11)".	<a href="#">7</a>
• <b>Table 3-1:</b> Changed "Fixed Gain Amplifier (Gain = ~3)" to "Fixed Gain Amplifier (Gain ≈ 3)".	<a href="#">7</a>
• <b>Table 3-1:</b> Removed "Product status" row and associated footnote.	<a href="#">7</a>
• <b>Section 3.1 (Related Products):</b> Added section.	<a href="#">9</a>
• <b>Section 4.1 (Pin Diagram):</b> Added section.	<a href="#">10</a>
• <b>Section 4.2 (Signal Descriptions):</b> Updated paragraph and NOTE.	<a href="#">11</a>
• <b>Table 4-1 (Signal Descriptions):</b> Updated DESCRIPTION of X1, $\overline{XRS}$ , $V_{DDIO}$ , GPIO20, GPIO21, GPIO24, GPIO30, GPIO31, and GPIO42.	<a href="#">11</a>
• <b>Table 4-1:</b> Added "Reserved" (–) rows.	<a href="#">11</a>
• <b>Section 5.1 (Absolute Maximum Ratings):</b> Added Input voltage, $V_{IN}$ (X1).	<a href="#">19</a>
• <b>Section 5.1:</b> Added Storage temperature, $T_{sig}$ .	<a href="#">19</a>
• <b>Section 5.1:</b> Updated description of "Input clamp current".	<a href="#">19</a>
• <b>Section 5.2 (ESD Ratings):</b> Changed section title from "Handling Ratings" to "ESD Ratings".	<a href="#">19</a>
• <b>Section 5.2:</b> Updated section.	<a href="#">19</a>
• <b>Section 5.3 (Recommended Operating Conditions):</b> Removed footnote that read, " $V_{DDIO}$ and $V_{DDA}$ should be maintained within approximately 0.3 V of each other."	<a href="#">20</a>
• <b>Section 5.4 (Power Consumption Summary):</b> Changed section title from "Current Consumption" to "Power Consumption Summary".	<a href="#">21</a>
• <b>Table 5-14 (Flash Parameters at 60-MHz SYSCLKOUT):</b> Added MAX Program Time values and MAX Erase Time values. Added footnote about program time. Added footnote about maximum flash parameter.	<a href="#">34</a>
• <b>Table 6-11 (Peripheral Frame 2 Registers):</b> Added SCI-B Registers and SCI-C Registers.	<a href="#">55</a>
• <b>Section 6.6.2 (Crystal Oscillator Option):</b> Added "The on-chip crystal oscillator X1 and X2 pins are 1.8-V level signals ..." paragraph.	<a href="#">63</a>
• <b>Figure 6-12 (External and PIE Interrupt Sources):</b> Updated figure.	<a href="#">69</a>
• <b>Figure 6-20 (Timing Example for Sequential Mode / Late Interrupt Pulse):</b> Changed "ADCCLK" to "ADC clock".	<a href="#">83</a>
• <b>Figure 6-21 (Timing Example for Sequential Mode / Early Interrupt Pulse):</b> Changed "ADCCLK" to "ADC clock".	<a href="#">84</a>
• <b>Figure 6-22 (Timing Example for Simultaneous Mode / Late Interrupt Pulse):</b> Changed "ADCCLK" to "ADC clock".	<a href="#">85</a>
• <b>Figure 6-23 (Timing Example for Simultaneous Mode / Early Interrupt Pulse):</b> Changed "ADCCLK" to "ADC clock".	<a href="#">86</a>

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• Table 6-72 (GPIOB MUX): Updated PERIPHERAL SELECTION 1 and PERIPHERAL SELECTION 2 of GPIO42. ....	129
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The datasheet number will be changing.

Device Family	Change From:	Change To:
TMS320F28050 - TMS320F28055	SPRS797B	SPRS797C

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TMS320F28050>

**Reason for Change:**

To accurately reflect device characteristics.

**Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):**

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

**Changes to product identification resulting from this PCN:**

None.

**Product Affected:**

TMS320F28050PNS	TMS320F28052FPNT	TMS320F28052PNT	TMS320F28054MPNQ
TMS320F28050PNT	TMS320F28052MPNQ	TMS320F28053PNS	TMS320F28054MPNT
TMS320F28051PNS	TMS320F28052MPNT	TMS320F28053PNT	TMS320F28054PNQ
TMS320F28051PNT	TMS320F28052PNQ	TMS320F28054FPNQ	TMS320F28054PNS
TMS320F28052FPNQ	TMS320F28052PNS	TMS320F28054FPNT	TMS320F28054PNT
TMS320F28055PNS	TMS320F28055PNT		

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