



Product/Process Change Notice - PCN 18_0020 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title: AD5675/76 and AD5671/72/75/76R Timing Spec Updates

Publication Date: 09-May-2018

Effectivity Date: 09-May-2018 *(the earliest date that a customer could expect to receive changed material)*

Revision Description:

Initial Release

Description Of Change:

Support for Vlogic down to 1.62V, with updated timing specs for I2C and SPI interfaces

Reason For Change:

Improve support for 1.8 Logic I/O by specifying Vlogic down to 1.8V -10% tolerance.

Impact of the change (positive or negative) on fit, form, function & reliability:

Tolerance on 1.8V logic supplies now extends below 1.8V. On I2C interface models, timing is now more aligned with standard.

Product Identification *(this section will describe how to identify the changed material)*

Timing Specification Changes will be reflected in Rev C AD5671R/75R and AD5675 data sheets and rev D AD5672R/76R and AD5676 data sheets.

Summary of Supporting Information:

See attached summary of datasheet changes.

Supporting Documents

Attachment 1: Type: Datasheet Specification Comparison

ADI_PCN_18_0020_Rev_-_ADI_PCN_18_0020_RevC_D_Timing Spec Changes.pdf

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:
PCN_Americas@analog.com

Europe:
PCN_Europe@analog.com

Japan:
PCN_Japan@analog.com

Rest of Asia:
PCN_ROA@analog.com

Appendix A - Affected ADI Models

Added Parts On This Revision - Product Family / Model Number (40)

AD5671R / AD5671RBCPZ-REEL7	AD5671R / AD5671RBCPZ-RL	AD5671R / AD5671RBRUZ	AD5671R / AD5671RBRUZ-REEL7	AD5672R / AD5672RBCPZ-REEL7
AD5672R / AD5672RBCPZ-RL	AD5672R / AD5672RBRUZ	AD5672R / AD5672RBRUZ-REEL7	AD5675 / AD5675ACPZ-REEL7	AD5675 / AD5675ACPZ-RL
AD5675 / AD5675ARUZ	AD5675 / AD5675ARUZ-REEL7	AD5675 / AD5675BCPZ-REEL7	AD5675 / AD5675BCPZ-RL	AD5675 / AD5675BRUZ
AD5675 / AD5675BRUZ-REEL7	AD5675R / AD5675RACPZ-REEL7	AD5675R / AD5675RACPZ-RL	AD5675R / AD5675RARUZ	AD5675R / AD5675RARUZ-REEL7
AD5675R / AD5675RBCPZ-REEL7	AD5675R / AD5675RBCPZ-RL	AD5675R / AD5675RBRUZ	AD5675R / AD5675RBRUZ-REEL7	AD5676 / AD5676ACPZ-REEL7
AD5676 / AD5676ACPZ-RL	AD5676 / AD5676ARUZ	AD5676 / AD5676ARUZ-REEL7	AD5676 / AD5676BCPZ-REEL7	AD5676 / AD5676BCPZ-RL
AD5676 / AD5676BRUZ	AD5676 / AD5676BRUZ-REEL7	AD5676R / AD5676RACPZ-REEL7	AD5676R / AD5676RACPZ-RL	AD5676R / AD5676RARUZ
AD5676R / AD5676RARUZ-REEL7	AD5676R / AD5676RBCPZ-REEL7	AD5676R / AD5676RBCPZ-RL	AD5676R / AD5676RBRUZ	AD5676R / AD5676RBRUZ-REEL7

Appendix B - Revision History

Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	09-May-2018	09-May-2018	Initial Release

Analog Devices, Inc.

DocId:4350 Parent DocId:None Layout Rev:7